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Intellectual Property Administration
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PATENT APPLICATION

ATTORNEY DOCKET NO. 10002861 -1

IN THE
UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor(s): Zhizhang Chen et al

Confirmation No.: 9286

Application No.: 09/817703

Examiner: Suk San Foong

Filing Date: Mar 26, 2001

Group Art Unit: 2823

Title: LDMOS And CMOS Integrated Circuit And Method Of Making

Commissioner for Patents
PO Box 1450
Alexandria, VA 22313-1450

PETITION FOR EXTENSION OF TIME

Sir:

In an Office Action mailed on May 02, 2003, on the above-identified U.S. Patent application, a shortened statutory period of three months was set for response. In accordance with 37 C.F.R. 1.136(a), applicant(s) hereby request(s) a:

- ☒ one month
- ☐ two months
- ☐ three months
- ☐ four months

time extension so that the period for response to the Office Action expires on Sep 02, 2003. Authorization to charge the fee required by 37 CFR 1.17 to Deposit Account 08-2025 appears in the enclosed transmittal letter. At any time during the pendency of this application, please charge any fees required or credit any over payment to Deposit Account 08-2025 pursuant to 37 CFR 1.25. Additionally please charge any fees to Deposit Account 08-2025 under 37 CFR 1.16 through 1.21 inclusive, and any other sections in Title 37 of the Code of Federal Regulations that may regulate fees. A duplicate copy of this sheet is enclosed.

I hereby certify that this correspondence is being deposited with the United States Postal Service as:

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Date of Deposit: 8/15/03

Typed Name: Timothy F. Myers

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02 FC:1251 110.00 DA

Respectfully submitted,

Zhizhang Chen et al

By Timothy F. Myers
Timothy F. Myers

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